	Type	L #	Hits	Search Text	DBs
1	BRS	L1	8920	multiple adj2 chip	US- PGPU B; USPA T
2	BRS	L2	99	1 near3 (image or sensor)	US- PGPU B; USPA T
3	BRS	L3	60	substrate and 2	US- PGPU B; USPA T
4	BRS	L4	1	("2005/0099532").URPN.	USPA T
5	BRS	L17	4816	1 and substrate	US- PGPU B; USPA T
6	BRS	L18		(first or second or top or bottom) adj2 substrate	US- PGPU B; USPA T

	Type	L #	Hits	Search Text	DBs
7	BRS	L19	109	1 same 18	US- PGPU B; USPA T
8	BRS	L20	6	19 and chamber	US- PGPU B; USPA T
9	BRS	L25	105	19 not 3	US- PGPU B; USPA T
10	BRS	L26	9472	18 same chip	US- PGPU B; USPA T
11	BRS	L27	172	26 same chamber	US- PGPU B; USPA T
12	BRS	L28	171	27 not (3 or 25)	US- PGPU B; USPA T

	Type	L #	Hits	Search Text	DBs
13	BRS	L29	39931	18 and chip	US- PGPU B; USPA T
14	BRS	L30	8477	29 and chamber	US- PGPU B; USPA T
15	BRS	L31	1925	30 and lens	US- PGPU B; USPA T
16	BRS	L32	546	31 and (transparent adj2 (layer or film))	US- PGPU B; USPA T
17	BRS	L33	321	32 and wire	US- PGPU B; USPA T
18	BRS	L34	17	thread and 33	US- PGPU B; USPA T

	Type	L #	Hits	Search Text	DBs
19	BRS	L35	0	("2005/0099659").URPN.	USPA T
20	BRS	L36	0	("2005/0099659").URPN.	USPA T
21	BRS	L37	1566	multiple adj2 chip	USOC R; FPRS; EPO; JPO; DERW ENT; IBM_T DB
22	BRS	L38	38356	(first or second or top or bottom) adj2 substrate	USOC R; FPRS; EPO; JPO; DERW ENT; IBM_T DB

	Type	L #	Hits	Search Text	DBs
23	BRS	L39	<b>4</b> 5	37 and 38	USOC R; FPRS; EPO; JPO; DERW ENT; IBM_T DB
24	BRS	L40	45	39 and substrate	USOC R; FPRS; EPO; JPO; DERW ENT; IBM_T DB